



FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE					ATTY. DOCKET NO. S00-086/US		SERIAL NO. 09/925,278	
LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary)					APPLICANT Subbhasish Mitra			
					FILING DATE 8/8/2001		GROUP 2133	
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME			RELEVANT INFORMATION	
A								
FOREIGN PATENT DOCUMENTS								
	2- letter code	DOCUMENT NUMBER	DATE	COUNTRY			TRANSLATION	
							YES	NO
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)								
B		[De Sousa 78] De Sousa, P.T., and F.P. Mathur, "Sift-Out-Modular Redundancy," IEEE Trans. Computers, Vol. C-27, No. 7, pp. 624-627, July 1978						
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L		[Trivedi 82] Trivedi, K.S., <i>Probability and Statistics with Reliability, Queuing, and Computer Science Applications</i> , Prentice Hall, Englewood Cliffs, NJ, USA, 1982.						
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	O	[Shehata] et al. "Rapid Prototyping of High Performance DSP Architectures Targeting FPGAs" in a paper published with Concordia University, Dept. of Electrical and Computer Engineering.
EXAMINER		DATE CONSIDERED
R. Stephen Gilmore		2 May 2005
<p>* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>		